IN THE SPECIFICATION:

On page 1, line 8, insert --This application is a continuation of Application No. 09/252,998, filed on February 19, 1999, *U.S. Pat. No. 6,032,214* ~~(still pending)~~, which is a continuation of Application No. 08/979,127, filed on November 26, 1997 (now U.S. Patent 5,915,105), which is a continuation of Application No. 08/762,139, filed on December 9, 1996 (now U.S. Patent 5,809,263); which is a continuation of Application No. 08/607,780, filed February 27, 1996 (now abandoned); which is a continuation of Application No. 08/222,646, filed on March 31, 1994 (now U.S. Patent 5,513,327); which is a continuation of Application No. 07/954,945, filed on September 30, 1992 (now U.S. Patent 5,319,755); which is a continuation of Application No. 07/510,898, filed on April 18, 1990 (now abandoned).--

On page 3, line 9, delete "micro-processor" and substitute --microprocessor--.

On page 6, line 1, delete "4,646,279" and substitute --4,646,270--.

On page 10, line 18, delete "Figure 7 shows" and substitute --Figures 7a and 7b show--.

On page 10, line 21, delete "Figure 8 shows" and substitute -- Figures 8a and 8b show--.

On page 34, line 4, after "devices" insert --do--.

On page 41, line 1, delete "or' "and substitute -- or--.

On page 45, line 17, delete "Fig. 7" and substitute --Figures 7a and 7b--.

On page 47, line 2, delete "Figure 8" and substitute

Page 2

--Figure 8a--.

On page 47, line 5, delete "from left to right" and substitute -- from right to left--.

On page 47, line 8, delete "right" and substitute --left--.

On page 47, line 9, delete the first "left" and substitute --right--.

On page 49, line 22, delete "primay" and substitute --primary--.

On page 54, line 13, delete "70" and substitute --69--.

On page 56, line 2, delete "Figure11" and substitute --Figure 11--.

On page 60, line 10, after "147" insert --A, B--.

IN THE CLAIMS:

Kindly **cancel** claims 1-150, without prejudice.

Kindly **add** the following claims:

1   --151.   A method of controlling a memory device, wherein

2   the memory device includes a plurality of memory cells, the

3   method of controlling the memory device comprises:

4   providing first block size information to the memory device,

5   wherein the first block size information defines a first amount

6   of data to be sampled by the memory device in response to a write

7   request;

8   issuing a first write request to the memory device, wherein

9   in response to the first write request the memory device inputs